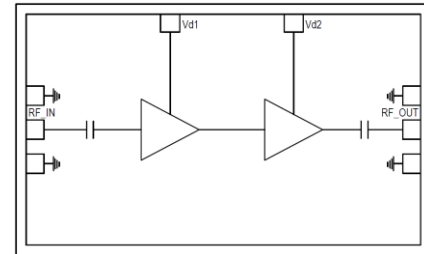


# 18 – 40 GHz Low Noise Amplifier

## Features

- ◆ Frequency Range : 18-40 GHz
- ◆ Nominal Gain : 11 dB
- ◆ Noise Figure : 3.5 dB
- ◆ I/O Return Losses : 20 dB
- ◆ Output P1dB : 6.5 dBm
- ◆ Self-bias operation
- ◆ DC decoupled Input and Output
- ◆ Chip Dimension: 1.95mm x 1.85mm x 0.1mm

## Functional Diagram



## Typical Applications

- ◆ Radar
- ◆ Military
- ◆ Test Equipment and Sensors.

## Description

The ASL1033 is a Low Noise Amplifier operating in 18.0 – 40.0 GHz frequency range. The LNA uses two stages of amplification and provides 11dB of gain with noise figure of less than 3.5dB having input & output return losses better than 15dB. The LNA has P1dB of 6.5dBm over the entire operating frequency band and operates on +3V or +4V DC supply with a current consumption of 53mA (typ). The circuit grounds on the die are provided through vias to the backside metallization.

## Absolute Maximum Ratings<sup>1</sup>

Parameter	Absolute Maximum	Units
Positive DC voltage	+6	V
RF input power	+20	dBm
Supply Current	100	mA
Operating Temperature	-55 to +85	°C
Storage Temperature	-65 to +150	°C

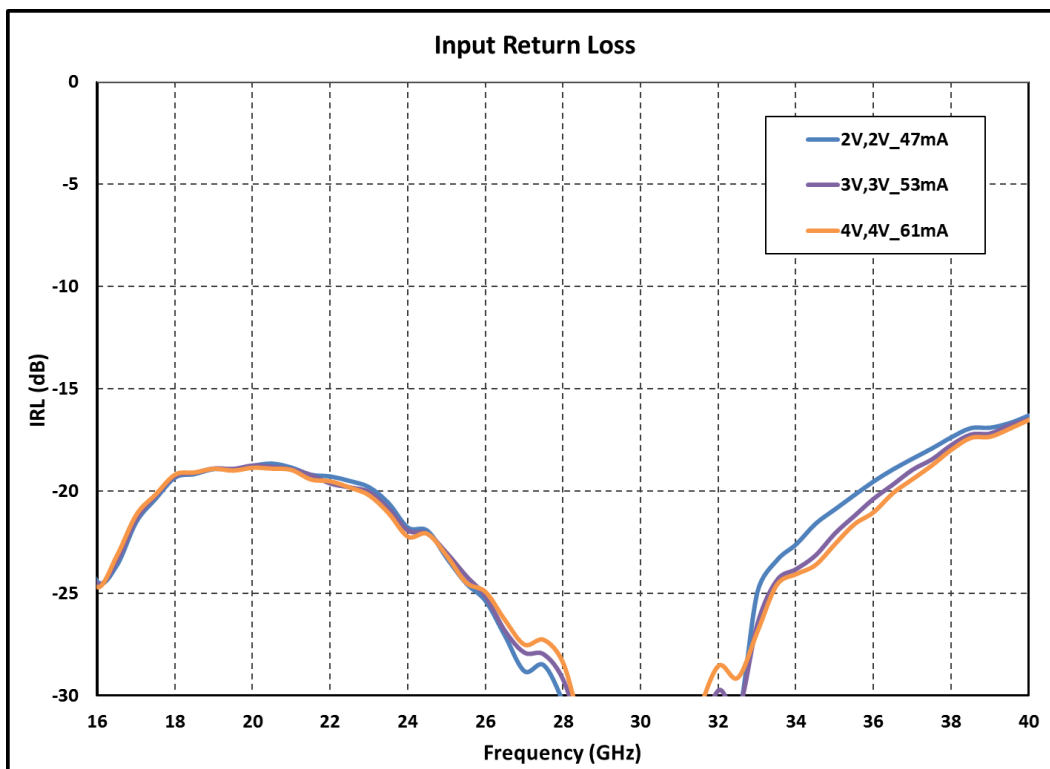
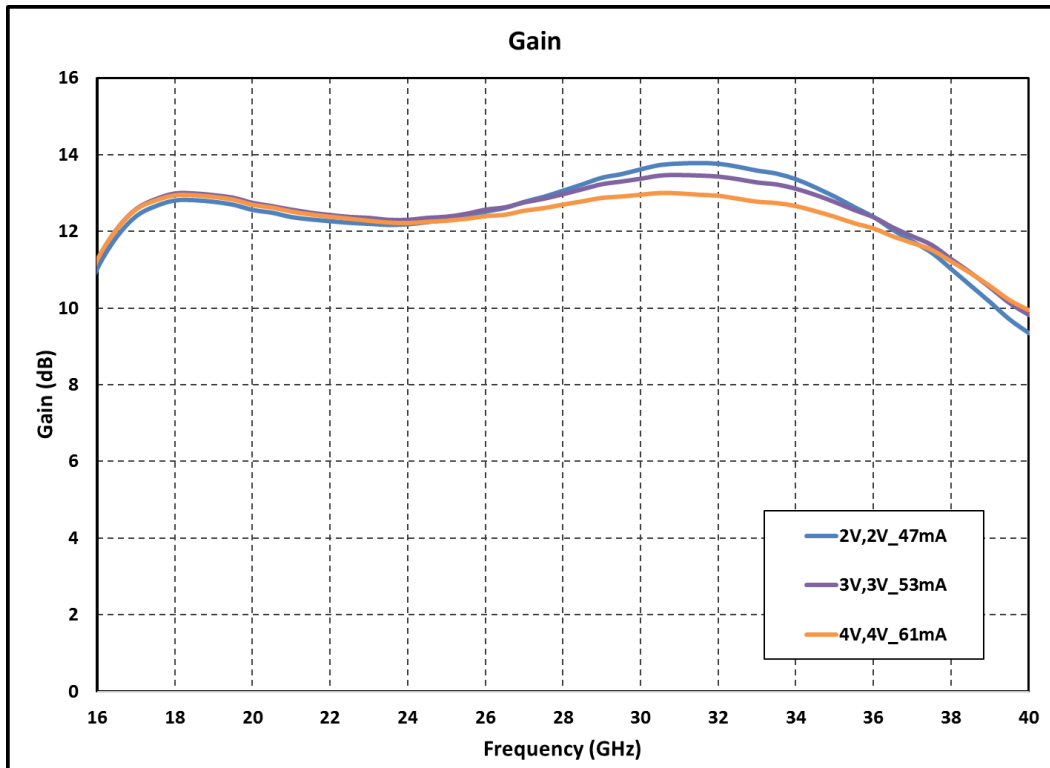
1. Operation beyond these limits may cause permanent damage to the component.

**Electrical Specifications @  $T_A = 25\text{ }^\circ\text{C}$ ,  $Z_o = 50\Omega$ ,**

Parameter	Min.	Typ.	Max.	Units
Frequency	18		40	GHz
Gain <sup>(1)</sup>	10	11	12	dB
Gain Flatness <sup>(1)</sup>	-	$\pm 1$	-	dB
Noise Figure <sup>(1)</sup>	2.8	3.5	4.5	dB
Input Return Loss <sup>(1)</sup>	-	20	-	dB
Output Return Loss <sup>(1)</sup>	-	20	-	dB
Output Power ( $P_1$ ,dB) <sup>(1)</sup>		5		dBm
Supply Voltage		3		V
Supply Current		53		mA

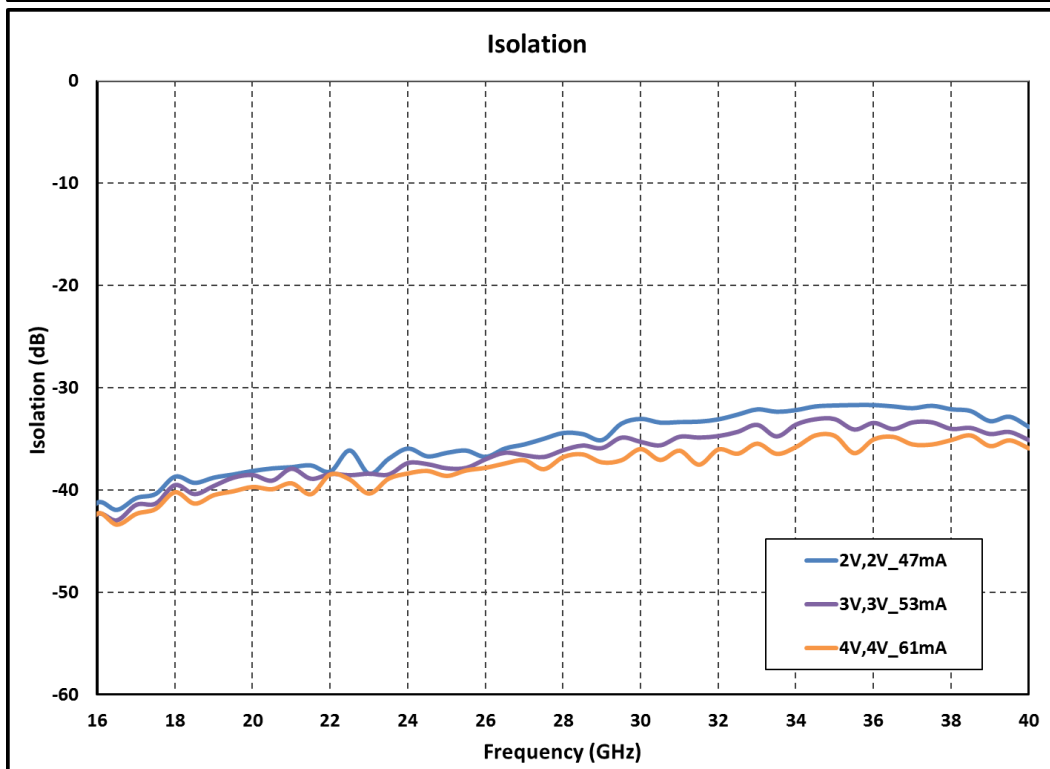
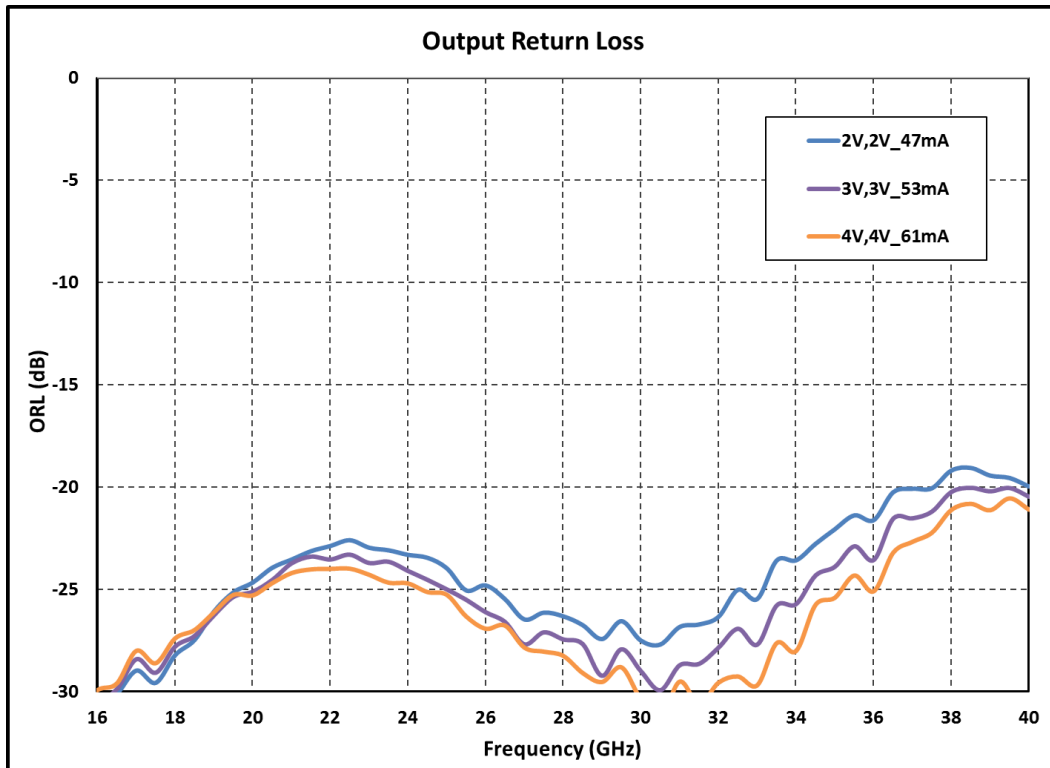
**Note:**

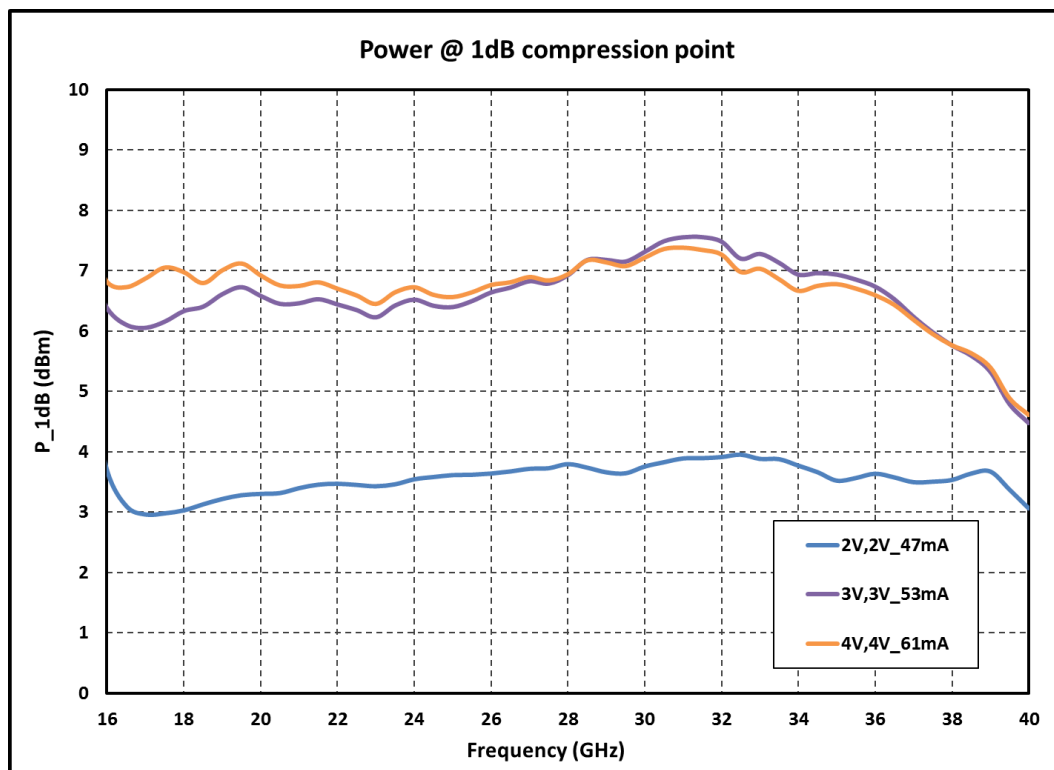
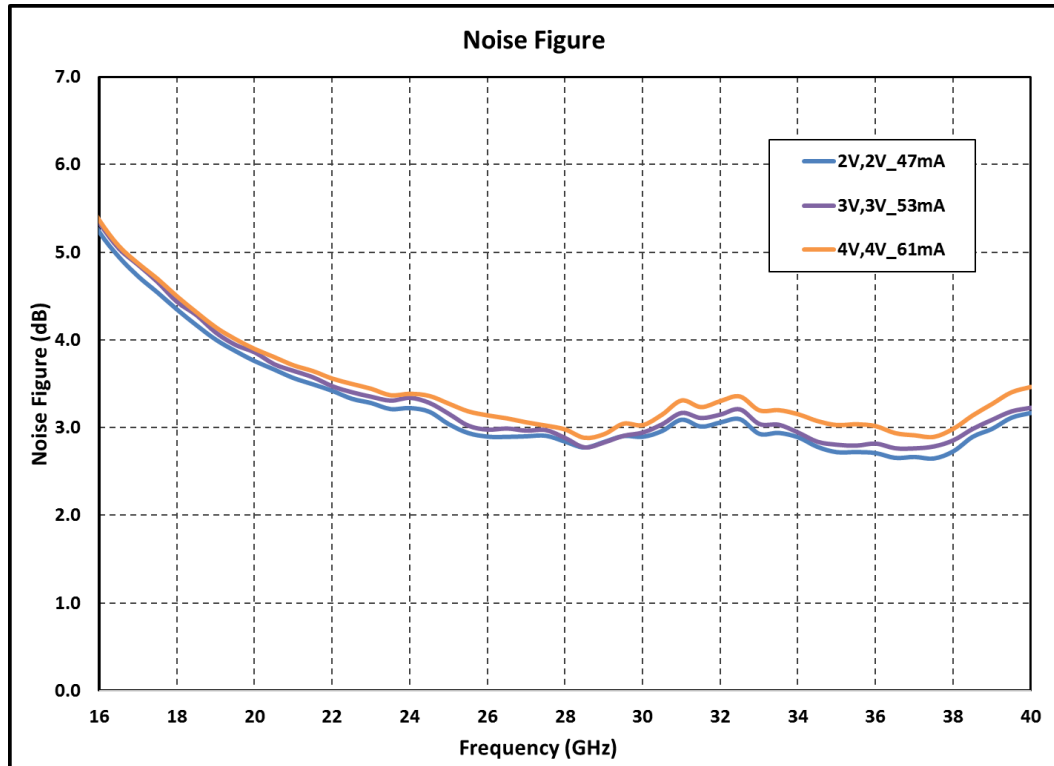
1. Electrical specifications as measured On-wafer.

**On-wafer Probed data**
*V<sub>d</sub>, V<sub>d2</sub>, V<sub>d3</sub> @ different Bias Voltages, Total Current = 50mA min, T<sub>A</sub> = 25 °C*


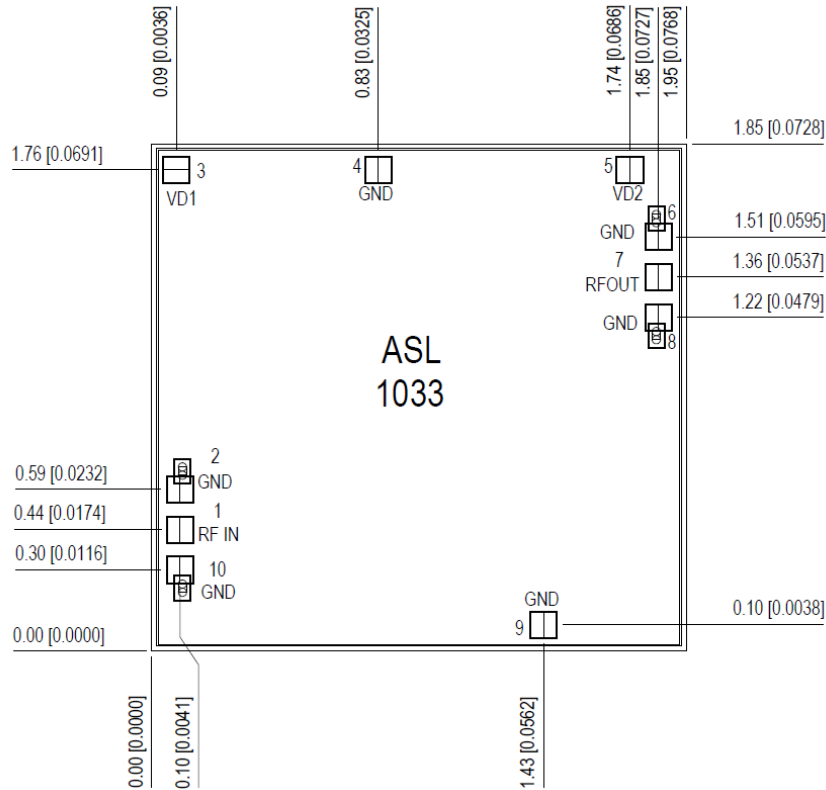
**On-wafer Probed data**

$V_d, V_{d2}, V_{d3}$  @ different Bias Voltages, Total Current = 50mA min,  $T_A = 25^\circ\text{C}$



**On-wafer Probed data**
*V<sub>d</sub>, V<sub>d2</sub>, V<sub>d3</sub> @ different Bias Voltages, Total Current = 50mA min, T<sub>A</sub> = 25 °C*


## Mechanical Characteristics



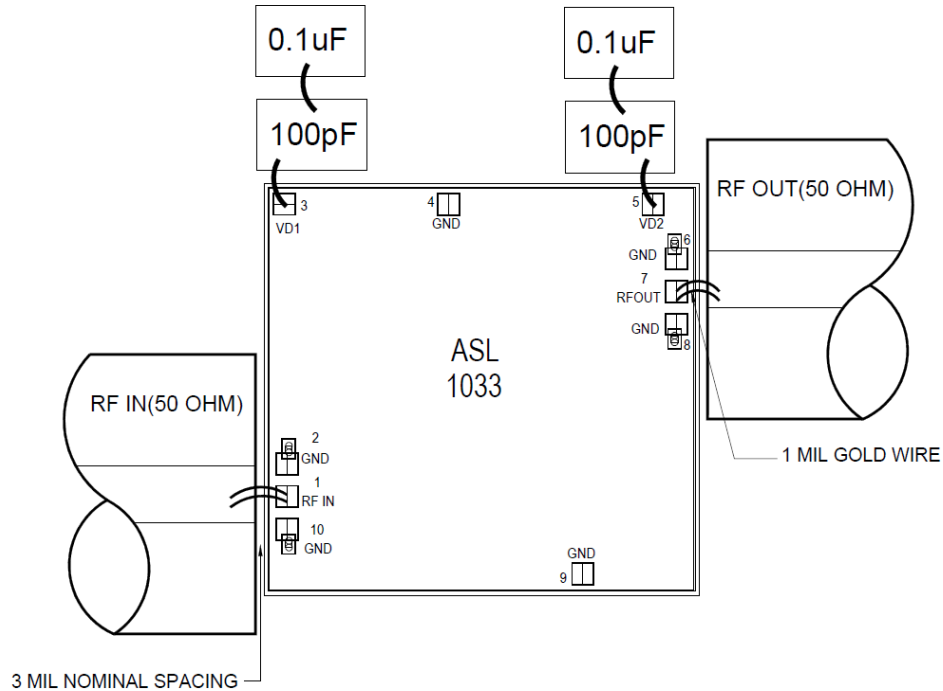
Units: millimeters (inches)

All RF and DC bond pads are 100µm x 100µm

Note:

1. Pad no. 1 : RF IN
2. Pad no. 3 : VD1
3. Pad no. 5 : VD2
4. Pad no. 7 : RF OUT
5. Pad no. 2, 4, 6, 8, 9, 10: GND

## Recommended Assembly Diagram


**Note:**

1. Two 1 mil (0.0254mm) bond wires of minimum length should be used for RF input and output.
2. 100pF and 0.1  $\mu$ F capacitors may be additionally used as a second level of bypass at the power supplies for reliable operation.

**Die attach:** For Epoxy attachment, use of a two-component conductive epoxy is recommended. An epoxy fillet should be visible around the total die periphery. If Eutectic attachment is preferred, use of fluxless AuSn (80/20) 1-2 mil thick preform solder is recommended. Use of AuGe preform should be strictly avoided.

**Wire bonding:** For DC pad connections use either ball or wedge bonds. For best RF performance, use of 150 - 200 $\mu$ m length of wedge bonds is advised. Single Ball bonds of 250-300 $\mu$ m though acceptable, may cause a deviation in RF performance.



***GaAs MMIC devices are susceptible to Electrostatic discharge. Proper precautions should be observed during handling, assembly & testing.***

All information and Specifications are subject to change without prior notice.

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